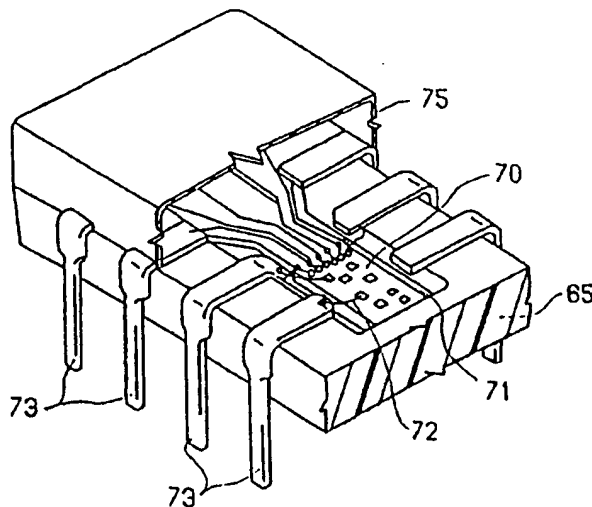




## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification <sup>5</sup> : <b>H01L 23/28, 23/48, 29/40</b>	<b>A1</b>	(11) International Publication Number: <b>WO 94/17552</b> (43) International Publication Date: <b>4 August 1994 (04.08.94)</b>
(21) International Application Number: <b>PCT/US94/01056</b> (22) International Filing Date: <b>28 January 1994 (28.01.94)</b> (30) Priority Data: 010,733                      29 January 1993 (29.01.93)                      US (71) Applicant: <b>ANADIGICS, INC. [US/US]; 35 Technology Drive, Box 4915, Warren, NJ 07059 (US).</b> (72) Inventors: <b>ROSENSTOCK, Michael, A.; 13 Huron Trail, Branchburg, NJ 08876 (US). WALLACE, Phillip, W.; 21 Woodland Road, Bernardsville, NJ 07924 (US). BAYRUNS, John, T.; 25 Hickory Place, Apartment B8, Chatham, NJ 07928 (US). SANYIGO, Kenneth, S.; 67 Mildred Lane, Fords, NJ 08863 (US). GILBERT, George, G.; RD1, Weather Hill Road, P.O. Box 303G, Lebanon, NJ 08833 (US).</b> (74) Agents: <b>REIN, Barry, D. et al.; Pennie &amp; Edmonds, 1155 Avenue of the Americas, New York, NY 10036 (US).</b>		(81) Designated States: <b>JP, KR, European patent (AT, BE, CH, DE, DK, ES, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).</b>  <b>Published</b> <i>With international search report.</i>

(54) Title: **PLASTIC PACKAGES FOR MICROWAVE FREQUENCY APPLICATIONS**

## (57) Abstract

A plastic package for microwave applications up to 14 Ghz is disclosed. The plastic package includes a plastic platform (65), a lead frame embedded on the surface of the platform (65) and, in one embodiment, a plastic cap (75) mounted on the platform (65) so as to seal the chip (70) within the package. The lead frame includes a baseplate (71) for mounting at least one semiconductor chip (70), at least one ground lead attached to the baseplate (71) and extending outwardly therefrom, and at least one signal lead for conducting signals to or from such semiconductor chip (70). The signal lead and at least one ground lead are configured as a microwave transmission line such as microwave coplanar strips, or microwave coplanar waveguide for transmitting microwave frequency signals. The package offers a low inductance ground path, good thermal characteristics, and low parasitic inductance and capacitance. It can be applied for high speed and high frequency applications.

**FOR THE PURPOSES OF INFORMATION ONLY**

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AT	Austria	GB	United Kingdom	MR	Mauritania
AU	Australia	GE	Georgia	MW	Malawi
BB	Barbados	GN	Guinea	NE	Niger
BE	Belgium	GR	Greece	NL	Netherlands
BF	Burkina Faso	HU	Hungary	NO	Norway
BG	Bulgaria	IE	Ireland	NZ	New Zealand
BJ	Benin	IT	Italy	PL	Poland
BR	Brazil	JP	Japan	PT	Portugal
BY	Belarus	KE	Kenya	RO	Romania
CA	Canada	KG	Kyrgyzstan	RU	Russian Federation
CF	Central African Republic	KP	Democratic People's Republic of Korea	SD	Sudan
CG	Congo	KR	Republic of Korea	SE	Sweden
CH	Switzerland	KZ	Kazakhstan	SI	Slovenia
CI	Côte d'Ivoire	LI	Liechtenstein	SK	Slovakia
CM	Cameroon	LK	Sri Lanka	SN	Senegal
CN	China	LU	Luxembourg	TD	Chad
CS	Czechoslovakia	LV	Latvia	TG	Togo
CZ	Czech Republic	MC	Monaco	TJ	Tajikistan
DE	Germany	MD	Republic of Moldova	TT	Trinidad and Tobago
DK	Denmark	MG	Madagascar	UA	Ukraine
ES	Spain	ML	Mali	US	United States of America
FI	Finland	MN	Mongolia	UZ	Uzbekistan
FR	France			VN	Viet Nam
GA	Gabon				

- 1 -

PLASTIC PACKAGES FOR  
MICROWAVE FREQUENCY APPLICATIONS

5

BACKGROUND OF THE INVENTION

10

The microelectronic revolution that began with the invention of transistors in the 1940s is now witnessing the incorporation of integrated circuits (ICs) into virtually every machine and appliance that people use daily, from cars and telephones to televisions and toasters. This large scale use of microprocessors in consumer electronics has been made possible in part by the development and manufacture of transfer molded plastic packages for ICs. Today, transfer molded plastic packages are used for more than ninety percent of all manufactured ICs and they continue to dominate the market for IC packages.

There are numerous advantages in using transfer molded plastic packages. They are small in size, easy to fabricate, can have large numbers of connecting leads, can be mass produced, and most importantly, can be made at low cost, typically only a few cents each in commercial quantities. This contrasts with costs of several dollars, typically, for higher performance packages, a cost which would prohibit the use of ICs in many consumer product applications.

A typical transfer molded plastic package consists of a lead frame, a semiconductor IC chip mounted on the lead frame, and an encapsulant enclosing the chip. The lead frame is the central supporting

- 2 -

structure of the package. It normally consists of a baseplate for mounting the IC chip and many conductive strips (i.e. leads) extending outwardly from the baseplate. The lead frame is usually made by stamping  
5 or chemically milling a thin sheet of metal such as a copper alloy. Prior to encapsulation, the chip is electrically connected to the leads by connecting thin gold wires between the bonding pads of the chip and the leads. The chip, lead frame and wires are then  
10 encapsulated in a thermoplastic material, exposing only the outer ends of the leads for connection. Accordingly, the outer ends of the leads can be formed into various shapes for inserting into a printed circuit board or for surface mounting.

15 The above-described conventional transfer molded plastic packages are not suitable for high speed or high frequency operations such as those involving microwave frequencies. A microwave package requires low parasitic coupling between the input and output leads of  
20 the package, a low inductance ground path, a high degree of isolation of the chip, and the ability to dissipate heat generated by the chip. In conventional transfer molded plastic packages, the closely spaced leads generate coupling between the leads. Additionally, the  
25 encapsulant produces capacitive and resistive coupling between the input and output leads, and the capacitive coupling increases with increased operating frequency. Further, the encapsulant undesirably provides a parasitic capacitance between the leads and the chip,  
30 therefore reducing the electrical isolation of the chip, which is especially harmful for microwave circuit chips requiring high gain. Moreover, the encapsulant also loads the small spiral inductors normally used on a microwave circuit chip, thereby undesirably changing the  
35 properties of the microwave circuits. As a result, it

- 3 -

precludes the use of such packages for complex multi-function microwave circuits.

Because conventional transfer molded plastic packages cannot be used for microwave circuits, other packages such as thin film or thick film ceramic packages are normally used. Compared with transfer molded plastic packages, ceramic packages are much more expensive to produce and are difficult to fabricate. Other alternative packages for microwave circuits such as commercially available TO-8 style packages are also much more expensive and generally cannot be surface mounted; they are therefore not suitable for applications that require surface mountable components.

Due to advances in microwave device and circuit technology, microwave circuits such as high performance GaAs Monolithic Microwave Integrated Circuits (MMICs) are being increasingly used in large numbers in applications such as cellular communications and Direct Broadcast Satellite (DBS) links. Some applications within these technologies have been foreclosed by lack of a commercially feasible low cost package for such circuits. Since the cost of GaAs microwave circuits has been drastically reduced in recent years due to the advancement of GaAs technology, the packaging cost of these circuits has increasingly become an important issue. Today, a packaged GaAs MMIC downconverter for satellite TV system costs only a few dollars or less; the cost of the package itself can significantly affect the cost of the packaged circuit and therefore the profitability and competitiveness of the product. By using lower cost packages, the cost of the packaged circuit can be greatly reduced. For example, by changing the package for a GaAs MMIC circuit from a ceramic leadless chip carrier package to a TO-5 package, the cost of the packaged circuit is reduced by

- 4 -

50 percent. However, TO-5 package is still much more expensive than the plastic packages. Accordingly, there has been a need, previously unmet, to provide a low cost, high performance, mass producible microwave package.

10

15

20

25

30

35

- 5 -

SUMMARY OF THE INVENTION

In accordance with the invention, a lead frame for a plastic package suitable for operation at 5 microwave frequencies is disclosed. The lead frame comprises a baseplate for mounting at least one semiconductor chip, at least one ground lead attached to the baseplate and extending outwardly therefrom, and at least one signal lead for conducting signals to or from 10 such semiconductor chip, said at least one signal lead and at least one ground lead being configured as a microwave transmission line.

More specifically, the signal lead and two ground leads may be configured as a microwave coplanar 15 waveguide wherein the signal lead is disposed between two parallel ground leads, and the width of the signal lead, the width of each of the two parallel ground leads, the gaps between the signal leads and the parallel ground leads, and the gap between the signal 20 lead and the baseplate are such that the resulting waveguide is suitable for transmitting microwave frequency signals. Alternatively, the signal lead and the ground lead can be configured as microwave coplanar strips wherein the signal lead is formed in parallel 25 with the ground lead, and the width of the signal lead, the ground leads and the gap between the signal and ground leads are similarly such that the resulting waveguide is suitable for transmitting microwave frequency signals.

30 A transfer molded plastic package in accordance with the invention comprises a lead frame including a baseplate, at least one ground lead attached to the baseplate and extending outwardly therefrom, and at least one signal lead for conducting signals to or 35 from such semiconductor chip, said at least one signal

- 6 -

lead and at least one ground lead being configured as a microwave transmission line, the baseplate being capable of having at least one semiconductor chip adhesively mounted thereon, interconnecting means for connecting  
5 the signal lead to such chip, and a plastic encapsulant encasing such chip and the interconnecting means.

In another embodiment of the invention, a plastic package comprises a plastic platform, a lead frame formed on the platform, the lead frame having a  
10 baseplate for mounting at least one semiconductor chip thereon, and a plastic cap adhesively mounted on the plastic platform and the lead frame to enclose the chip therein. The lead frame may also comprise at least one ground lead attached to the baseplate and extending  
15 outwardly therefrom, and at least one signal lead for conducting signals to or from such semiconductor chip, said at least one signal lead and at least one ground lead being configured as a microwave transmission line.

Finally, a method for packaging a  
20 semiconductor chip is disclosed, comprising the steps of fabricating a lead frame including a baseplate and a plurality of leads, forming a plastic platform to embed the lead frame and to expose a top surface of the baseplate for mounting the chip, mounting the  
25 semiconductor chip on the baseplate, electrically connecting the chip with the leads, and mounting a plastic cap on the platform to seal the chip therein.

30

35



- 7 -

BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects, features and advantages of the invention will be more apparent from the following detailed description in conjunction with the appended drawings in which:

Fig. 1 is a top view of a preferred embodiment of a lead frame of the present invention;

Fig. 2 is a three-dimensional view of one embodiment of the plastic package of the present invention;

Figs. 3a-c are cross-sectional views of a conventional transfer molded plastic package, the plastic package of the present invention, and an alternative embodiment of the package of the present invention, respectively;

Figs. 4a-f depict top and side views of the lead frame molded on a plastic platform, top and side views of a plastic cap, and top and side views of a preferred embodiment of the plastic package of this invention;

Figs. 4g and h depict top and cross-sectional views of a plastic cap with chamfered exterior;

Fig. 5 illustrates a measured time domain reflection versus time for a GaAs coplanar transmission line mounted in a plastic package of the present invention, and in a conventional T0-5 package; and

Fig. 6 displays the measured conversion gain and noise figure versus frequency for a Ku-band GaAs downconverter mounted in a plastic package of the present invention.

- 8 -

DETAILED DESCRIPTION

In one preferred embodiment of the invention, as depicted in Fig. 1, a lead frame for a quad plastic package for microwave frequency operation comprises a baseplate 10 for mounting a semiconductor circuit chip with ground leads 11, 13, 20, 25, 40, 50 and 55 attached to the baseplate. A first coplanar waveguide 15 is shown for transmitting a first microwave signal to or from the semiconductor chip, as are an intermediate frequency lead 45 for transmitting an intermediate frequency to or from the chip, a dc voltage input lead 60, and two leads 30 and 35 for local oscillators. A periphery portion 5 connects to all the outer ends of the leads to hold the lead frame in place; it will be cut off from the lead frame after the package is formed.

Illustratively, first coplanar waveguide 15 comprises a signal lead 12 disposed between two parallel ground leads 11 and 13. In the waveguide, the two parallel ground leads 11 and 13 have the same width. The signal lead is symmetrically formed between the two parallel leads. Additionally, the width of the signal lead, the width of the two parallel ground leads, the spacings between the signal lead and the parallel leads, and the spacing between the baseplate and the signal lead, are designed to make the waveguide suitable for microwave frequency operation up to about 14GHz. The design of microwave coplanar waveguides has been well known and has been described in numerous publications, for example, by K.C. Gupta in Microstrip Lines and Slotlines, Chapter 7, Artech House, 1979.

Ground leads 11, 13, 20, 25, 40, 50 and 55 provide ground potential to the baseplate and the package. Illustratively, at least one ground lead extends outwardly from each of the four sides of this

- 9 -

quad lead frame. This configuration advantageously provides a low inductance ground path to the baseplate. In addition, the ground leads efficiently dissipate heat generated from the chip mounted at the center of the baseplate. As a result, this lead frame provides low inductance ground path as well as good thermal characteristics.

Lead 45 is used for transmitting an intermediate frequency microwave signal. It is formed at the opposite side of the baseplate from the coplanar waveguide for minimizing any coupling between the intermediate frequency signals and those on the waveguide. Dc voltage lead 60 is used to provide a dc voltage to the lead frame. Local oscillator leads 30 and 35 are interconnected and they are used to connect the chip with an external oscillator. Preferably, to reduce coupling between the leads, the dc voltage lead, the intermediate frequency lead, the leads for local oscillator, and the signal lead of the waveguide are all separated from each other by at least one ground lead.

In this embodiment, the waveguide can be utilized for either importing a microwave signal to the chip, or for exporting a microwave signal from the chip. Similarly, the intermediate frequency lead can be used for either importing or exporting a signal of intermediate frequency from or to the chip. For example, if the semiconductor chip is a GaAs downconverter, a 12GHz microwave signal is imported through the coplanar waveguide to the chip, and a 1GHz intermediate frequency signal is provided from the chip to circuitry outside of the package through the intermediate frequency lead. Alternatively, the intermediate frequency lead may be used for importing a 1 GHz intermediate frequency signal, and a 12 GHz microwave signal may be exported from the waveguide.

- 10 -

The intermediate frequency lead in the above described lead frame can be replaced by a second coplanar microwave waveguide formed in the same fashion as the first coplanar waveguide. Such a lead frame is  
5 utilized when both the input signal to the chip and the output signal from the chip are microwave frequency signals.

In another alternative embodiment, microwave coplanar strips are used to transmit microwave signals  
10 to or from the chip. The microwave coplanar strips each comprise a signal lead formed parallel to a ground lead; the width of the signal and ground leads, as well as the gap between them, are made such that the  
resulting waveguide is suitable for microwave frequency  
15 operation.

Microwave coplanar strips and microwave coplanar waveguides may be used interchangeably, in accordance with the invention, and both types of waveguide may be used in the same package.

20 The above-described lead frame can also be utilized for high speed digital applications. For example, a high speed data stream can be transmitted to or from the chip via the coplanar waveguide or the coplanar strips.

25 One application of the above-described lead frame is in a transfer molded plastic package. In forming this package, a semiconductor chip is first mounted at the center of the baseplate with adhesives or solder. Electrical connections between the chip and the  
30 leads are formed by bonding gold wires from the leads to bonding pads on the chip. The chip, the baseplate, and the wires are then encapsulated by transfer molding of thermoplastic encapsulant. The total number of leads in this package can be made to be eight, twelve, sixteen or  
35 more to accommodate the applicable industry standard.

- 11 -

In another embodiment of this invention, a plastic package for a semiconductor chip comprises a lead frame formed on a plastic platform, and a plastic cap adhesively mounted over the lead frame and the platform to enclose the chip. As compared with conventional transfer molded plastic packages, this package provides reduced parasitic resistance and capacitance between input and output leads due to the absence of any encapsulant. Additionally, there is no dielectric loading effect on the chip. Therefore, this package is suitable for high speed or high frequency applications. Another advantage of this package over conventional transfer molded plastic packages is, because the plastic is not in direct contact with the chip, that the chip is free from encapsulant-induced stress due to the thermal expansion mismatch between the chip and the plastic.

Fig. 2 is a three-dimensional view of this package. The package comprises a plastic platform 65, a lead frame comprising baseplate 71 embedded in the platform and a plurality of leads 73, and a plastic cap 75 adhesively mounted on the platform. A semiconductor chip 70 is mounted on baseplate 71. Electrically conductive wires 72 such as gold or aluminum wires are bonded from the leads to the chip to provide electrical connection to the chip. Illustratively, the lead frame is a conventional lead frame; alternatively, a lead frame having leads configured as a microwave transmission line (such as the one depicted in Fig. 1) can be used in this package.

Figs. 3a and b depict cross-sectional views respectively of a conventional transfer molded plastic package and a plastic package in accordance with the present invention, respectively. For convenience, like elements in this figure are given the same designations

- 12 -

as in Fig. 2. As shown in Fig. 3a, in a conventional transfer molded plastic package, chip 70 is mounted on a baseplate 71 of a lead frame and is in direct contact with an encapsulant 74. Alternatively, a stress relieving material such as commercially available Dow Corning 6646 can be first applied to surround the chip prior to the encapsulation. In this case, the chip is in direct contact with the stress relieving material and not the encapsulant. Leads 76 and 77 transmit input signal lead and output signal lead, respectively. Capacitor C and resistor R represent parasitic capacitive and resistive coupling, respectively, between the input lead and output lead. As shown in Fig. 3b, the parasitic capacitor C and resistor R are essentially eliminated in the package of the present invention by the utilization of a plastic cap 75. As a result, the package of the present invention is suitable for high speed and high frequency applications.

Fig. 3c illustrates an alternative embodiment of the package depicted in Fig. 3b. In this package, the platform comprises two components, a slug 64 formed substantially under and in contact with baseplate 71, and an annular plastic portion 63 surrounding the slug. Preferably, the slug is a metal slug and it functions as ground and a heat sink and it is especially useful when the chip is a high power chip. The slug can be soldered onto a circuit board when assembling this package.

Figs. 4a-f depict a preferred embodiment of this plastic package. The package consists of three components, a lead frame 80, a thermoplastic platform 85, and a plastic cap 90 adhesively mounted on the lead frame. The outer dimensions of this package are substantially similar to the industrial standard Small Quad Flat Package (SQFP); thus this package can conveniently replace the SQFP.

- 13 -

The top and side views of the lead frame molded onto the plastic platform are illustrated in Figs. 4a and b. The lead frame is the same lead frame as illustrated in Fig. 1, and thus is not described in detail here. The lead frame is molded onto thermoplastic platform 85. The outer ends of the leads from the lead frame may advantageously be bent to form gull-wing shaped ends for surface mounting as shown in Fig. 4b. A semiconductor chip such as a GaAs downconverter can be adhesively mounted at the center and on a top surface 84 of the lead frame; and the chip is electrically connected with the lead frame by wire bonding between the bonding pad on the chip and the portions of the leads that are closest to the baseplate.

The top and cross-sectional views of the plastic cap are depicted in Figs. 4c and d. A circular dent 91 formed at the outer surface of the cap is an index feature for package assembly. An index feature can also be formed on the lead frame for identifying the leads.

As depicted in Figs. 4g and h, a cap has chamfered exterior can be utilized in the package. Chamfered cap prevents cracks at the corners of the cap and reduces manufacturing difficulties.

In this package, both the platform and the cap are made of conventional Novolac thermoplastic. The lead frame may be made of any copper alloy such as the one conventionally designated as OLIN. This material provides good thermal and electrical conductivities.

The plastic package of the invention may be fabricated utilizing well developed packaging technology. First, The lead frame is made by appropriate chemical milling of a thin sheet of copper alloy such as OLIN. Alternatively, the lead frame can also be manufactured by stamping from a sheet of copper

- 14 -

alloy. The fabricated lead frame is then transferred to a transfer-molding press for forming the plastic platform on the lead frame.

A semiconductor chip is then mounted approximately at the center of the baseplate of the lead frame by conductive epoxy such as silver filled epoxy or solder. Wire bonding is subsequently performed to connect the chip to the leads. Finally, the plastic cap is adhesively mounted on the plastic platform, sealing the chip in the cavity formed between the cap and the platform.

The package of the invention displays excellent microwave frequency characteristics and thermal characteristics. Microwave frequency operation up to 14GHz, and maximum power up to 1W have been achieved with a GaAs downconverter enclosed in the package. These values are not intended as limits on the scope of the invention.

In Figure 5, the frequency response of this package is measured and compared with the frequency response of a conventional TO-5 ceramic package for microwave application. In this experiment, a fast rising pulse is transmitted to the package that contains a GaAs coplanar transmission line which is terminated, and the reflection of the pulse is measured against time from the starting of the pulse. In this figure, y axis represents the time domain reflection (or step response) and the x-axis the time. Curve A in the figure is the measured reflection of a GaAs coplanar transmission line mounted in the quad plastic package illustrated in Figs. 4a-f. For comparison, the measured reflection of the same kind of GaAs coplanar transmission line mounted in a conventional TO-5 package is shown in the same drawing as curve B. Peaks 100 and 105 in the drawing reflect the amplitudes of the parasitics of the plastic package



- 15 -

of the invention and the TO-5 package, respectively. As shown, the parasitics of the plastic package and the much more expensive TO-5 package are comparable, showing that the former is suitable for microwave applications.

5           Fig. 6 shows the measured Conversion Gain and Noise Figure (in dB) versus intermediate frequency (in MHz), respectively, for a Ku-Band downconverter for Direct Broadcast Satellite mounted in a quad plastic package in accordance with the invention. The RF  
10 frequency for this test is approximately between 10.7 and 11.8 GHz, and the local oscillator frequency is 9.75GHz. The high conversion gain and low noise figure demonstrate the excellent microwave frequency characteristics of the plastic package.

15           The above-described preferred embodiment has twelve leads. Alternatively, a plastic package having eight, sixteen, more than sixteen, or any desired number of leads can also be similarly constructed. Packages having eight leads generally include one central signal  
20 lead and two ground leads for the coplanar waveguide, one lead for dc input, one lead for local oscillator, and one intermediate frequency lead. For the package with sixteen leads, the leads ordinarily consist of one central signal lead and two ground leads for the  
25 coplanar waveguide, one intermediate frequency lead, two leads for local oscillator, one lead for dc voltage input, and nine other ground leads.

          As will be apparent to those skilled in the art, numerous modifications may be made within the scope  
30 of the invention, which is not intended to be limited except in accordance with the following claims.

- 16 -

What is claimed is:

1. A lead frame for a plastic package suitable for operation in the microwave frequency range  
5 comprising:

a baseplate for mounting at least one semiconductor chip;

at least one ground lead attached to the baseplate and extending outwardly therefrom; and

10 at least one signal lead for conducting signals to or from such semiconductor chip, said at least one signal lead and at least one ground lead being configured as a microwave transmission line.

---

15 2. The lead frame of claim 1 wherein the signal lead and the ground lead are configured as microwave coplanar strips wherein the signal lead is formed parallel to the ground lead, and the width of the signal lead, the ground lead and the gap between the  
20 signal and ground leads are such that the leads form a waveguide suitable for transmitting microwave frequency signals.

3. The lead frame of claim 1 including at  
25 least two parallel ground leads, wherein the signal lead and two ground leads are configured as a microwave coplanar waveguide with the signal lead disposed between said two parallel ground leads, and the width of the signal lead, the width of each of the two parallel  
30 ground leads, the gaps between the signal leads and the parallel ground leads, and the gap between the signal lead and the baseplate are such that the leads form a waveguide suitable for transmitting microwave frequency signals.

35

- 17 -

4. The lead frame of claim 3 further comprising an intermediate frequency lead for transmitting an intermediate frequency signal.

5 5. The lead frame of claim 4 wherein the coplanar waveguide and the intermediate frequency lead extend out from two opposite sides of the baseplate for reducing coupling therebetween.

10 6. The lead frame of claim 5 made of copper alloy.

~~7. The lead frame of claim 4 wherein the~~  
total number of leads is eight.

15 8. The lead frame of claim 4 wherein the total number of leads is twelve, consisting of one signal lead and two ground leads forming a coplanar waveguide, one intermediate frequency lead, two leads  
20 for local oscillator signal, one lead for dc voltage input, and five additional ground leads.

9. The lead frame of claim 4 wherein the total number of leads is 16.

25 10. A transfer molded plastic package for a semiconductor chip comprising:

a lead frame including a baseplate, at least one ground lead attached to the baseplate and extending  
30 outwardly therefrom, and at least one signal lead for conducting signals to or from such semiconductor chip, said at least one signal lead and at least one ground lead being configured as a microwave transmission line;  
at least one semiconductor chip mounted on the  
35 baseplate;

- 18 -

interconnecting means connecting the leads to the chip; and

a plastic encapsulant encasing the chip and the interconnecting means.

5

11. The package of claim 10 wherein the signal lead and the ground lead are configured as microwave coplanar strips wherein the signal lead and the ground lead are formed parallel to each other, and  
10 the width of the signal and ground leads and the gap between the leads are such that the leads form a waveguide suitable for transmitting microwave frequency signals.

15

12. The package of claim 10 wherein the signal lead and two ground leads are configured as a microwave coplanar waveguide wherein the signal lead is disposed between two parallel ground leads, and the width of the central signal lead, the width of the two  
20 parallel ground leads, the gaps between the signal lead and the parallel ground leads, and the gap between the signal lead and the baseplate are such that the leads form a waveguide suitable for transmitting microwave frequency signals.

25

13. The package of claim 12 wherein the interconnecting means for connecting the leads to the chip includes bonded wires.

30

14. The package of claim 12 wherein the lead frame further includes an intermediate frequency lead interconnected to the chip for transmitting intermediate frequency signals.

35

15. A plastic package comprising:

- 19 -

a plastic platform;

a lead frame formed on the platform, the lead frame having a baseplate for mounting at least one semiconductor chip; and

5 a plastic cap mounted on the platform and the lead frame to enclose and seal the chip therein.

16. The package of claim 15 wherein the lead frame is embedded in the plastic platform, and a top surface of the lead frame is substantially level with the top surface of the platform, exposing the top surface for mounting the semiconductor chip.

17. The package of claim 15 wherein the lead frame further comprises at least one ground lead attached to the baseplate and extending outwardly therefrom, and at least one signal lead for conducting signals to or from such semiconductor chip, said at least one signal lead and at least one ground lead being configured as a microwave transmission line.

18. The package of claim 15 wherein the cap has an chamfered exterior.

19. The package of claim 17 wherein the signal lead and the ground lead are configured as microwave coplanar strips wherein the signal lead is formed parallel to the ground lead, and the widths of the signal lead, the ground leads and the gaps between the signal and ground leads are such that the leads form a waveguide suitable for transmitting microwave frequency signals.

20. The package of claim 17 wherein the signal lead and two parallel ground leads are configured

- 20 -

as a microwave coplanar waveguide wherein the signal lead is disposed between two parallel ground leads, and the width of the signal lead, the width of each of the two parallel ground leads, the gaps between the signal  
5 lead and the parallel ground leads, and the gap between the signal lead and the baseplate are such that the leads form a waveguide suitable for transmitting microwave frequency signals.

10           21. The package of claim 20 further comprising an intermediate frequency signal lead for transmitting intermediate frequency signals to and from the chip.

---

15           22. The package of claim 21 wherein the coplanar waveguide and intermediate frequency lead extend out from two opposite sides of the baseplate for reducing coupling therebetween.

20           23. The package of claim 22 wherein the lead frame is made of copper alloy.

            24. The package of claim 23 wherein the total number of leads is eight.

25           25. The package of claim 24 wherein the total number of leads is twelve, consisting of one signal lead and two ground leads for the waveguide, one intermediate frequency lead, two leads for local oscillator signal,  
30 one lead for dc voltage input, and five additional ground leads.

            26. The package of claim 25 wherein the total number of leads is 16.

35

- 21 -

27. A method for packaging a semiconductor chip comprising the steps of:  
fabricating a lead frame comprising a baseplate and a plurality of leads;  
5 embedding the lead frame in a plastic platform so that a top surface of the baseplate is exposed for mounting the chip;  
mounting the chip on the baseplate;  
electrically connecting the chip with the  
10 leads; and  
mounting a plastic cap on the platform and lead frame to seal the chip therein.

---

28. The method of claim 27 wherein the step  
15 of fabricating the lead frame comprises chemically milling a sheet of metal.

29. The method of claim 28 wherein the step  
of fabricating the lead frame comprises stamping a sheet  
20 of metal.

30. The method of claim 27 wherein the step  
of connecting the chip with the leads includes wire  
bonding.

25

31. The method of claim 27 wherein the lead frame includes at least one ground lead attached to the baseplate and at least one signal lead for conducting signals to or from such semiconductor chip, said at  
30 least one signal lead and at least one ground lead being configured as a microwave transmission line.

32. The method of claim 31 wherein the signal lead and the ground lead are configured as microwave  
35 coplanar strips wherein the signal lead is formed

- 22 -

parallel to the ground lead, and the widths of the  
signal lead, the ground leads and the gap between the  
signal and ground leads are made such that the leads  
form a waveguide suitable for transmitting microwave  
5 frequency signals.

33. The method of claim 31 wherein the signal  
lead and two ground leads are configured as a microwave  
coplanar waveguide wherein the signal lead is disposed  
10 between two parallel ground leads, and the width of the  
signal lead, the width of each of the two parallel  
ground leads, the gaps between the signal leads and the  
~~parallel ground leads, and the gap between the signal~~  
lead and the baseplate are made such that the leads form  
15 a waveguide suitable for transmitting microwave  
frequency signals.

20

25

30

35



1/8

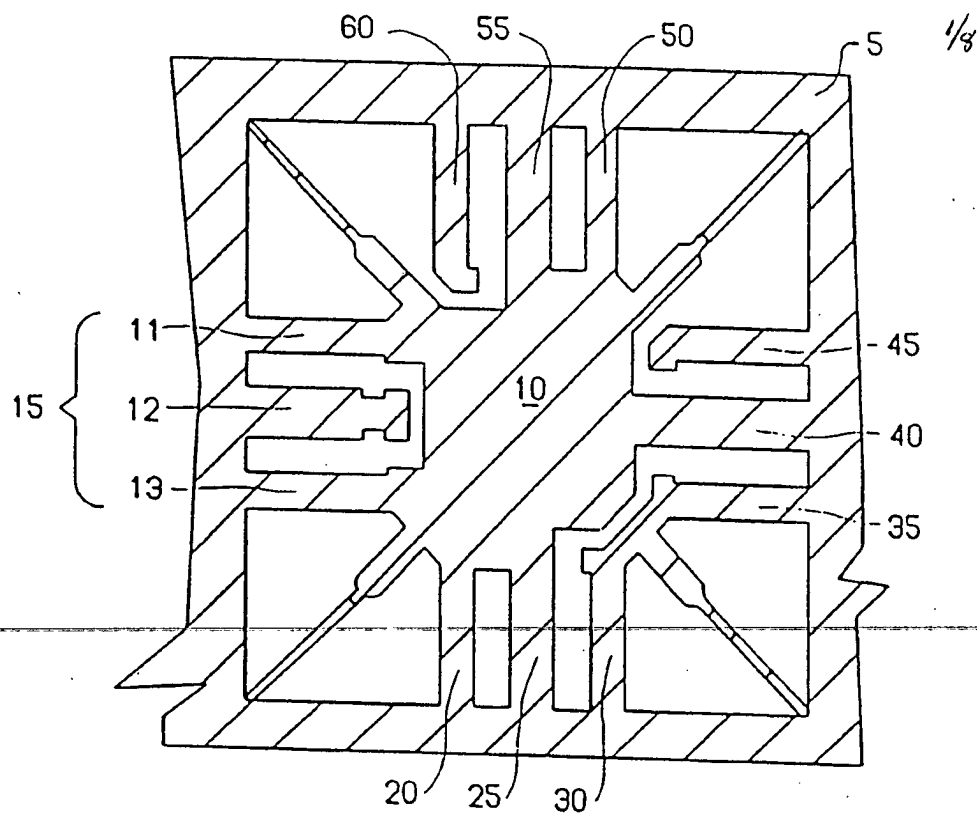


FIG. 1

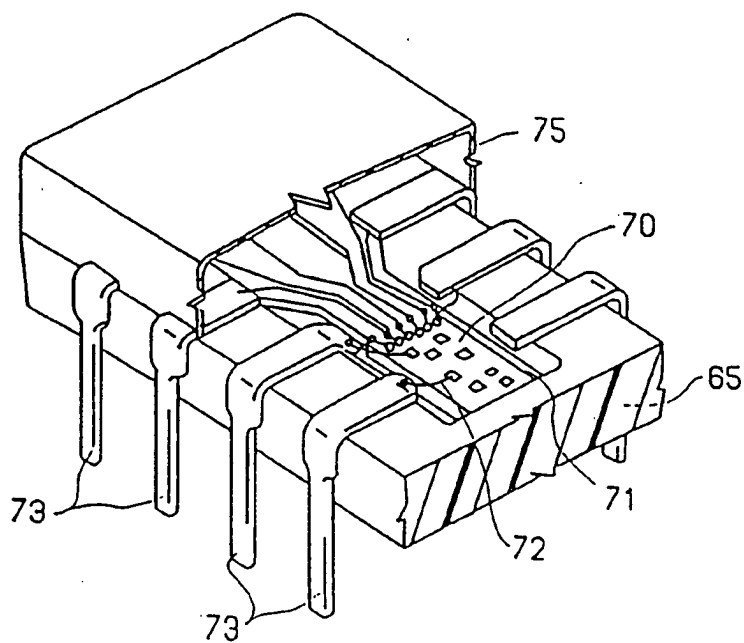


FIG. 2

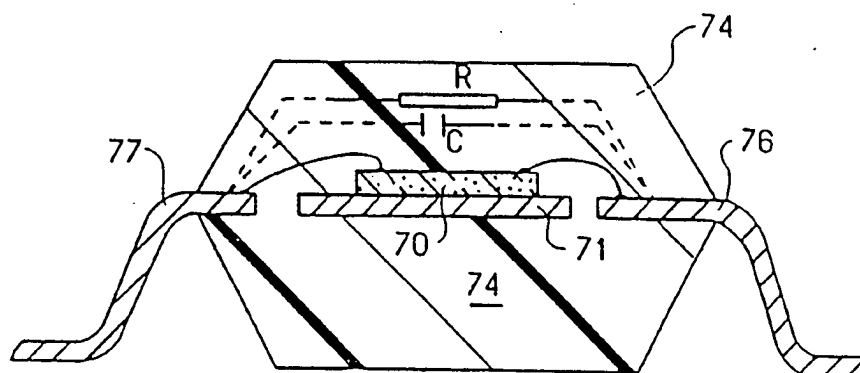


FIG. 3a

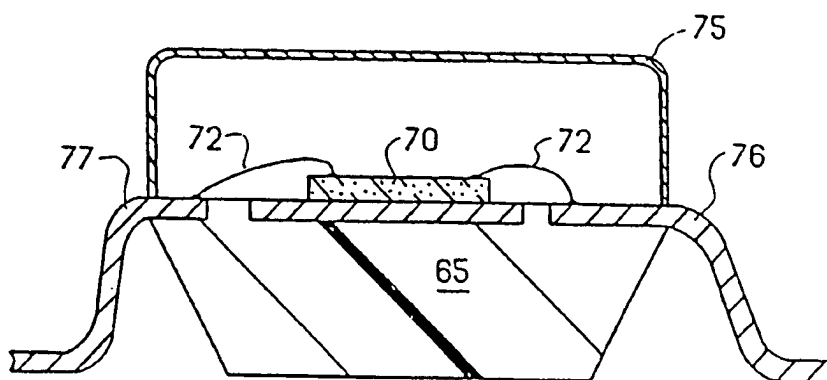


FIG. 3b

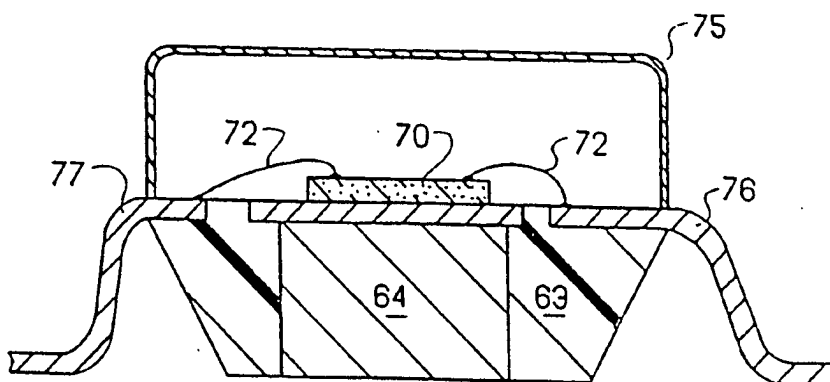


FIG. 3c

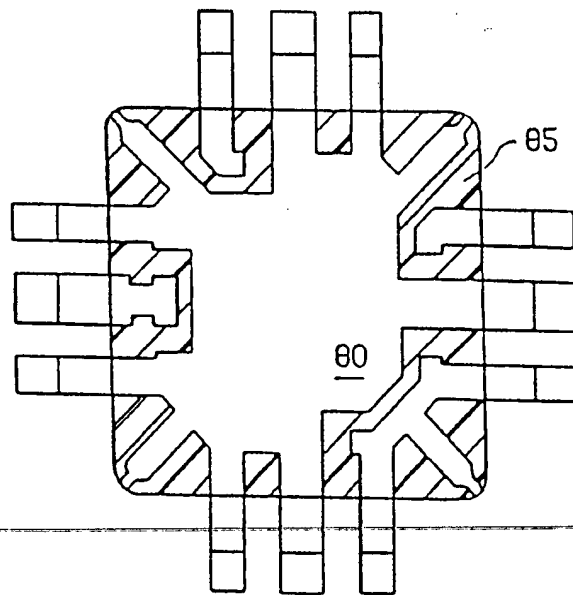


FIG. 4a

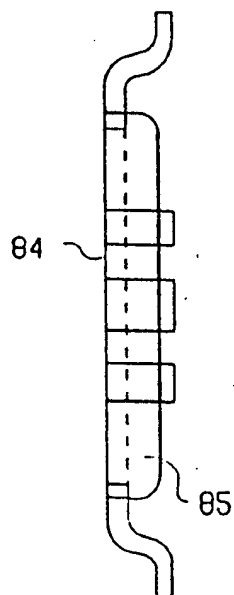


FIG. 4b

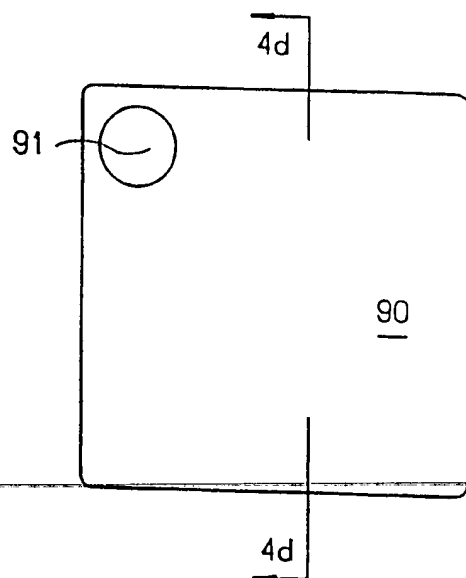


FIG. 4c

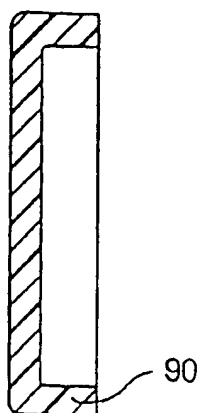


FIG. 4d

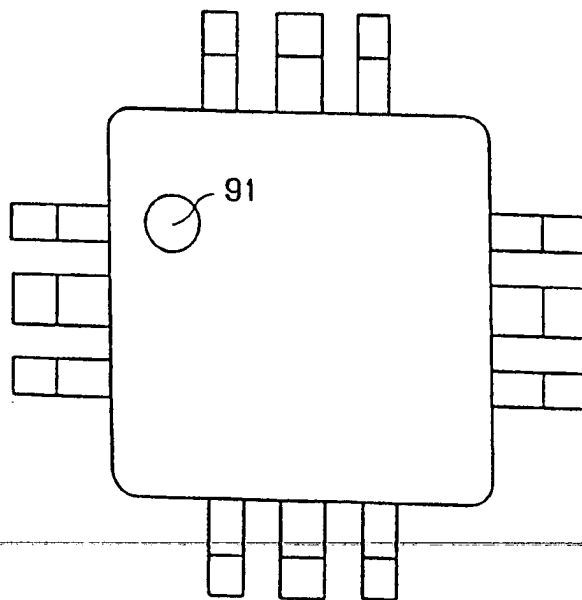


FIG. 4e

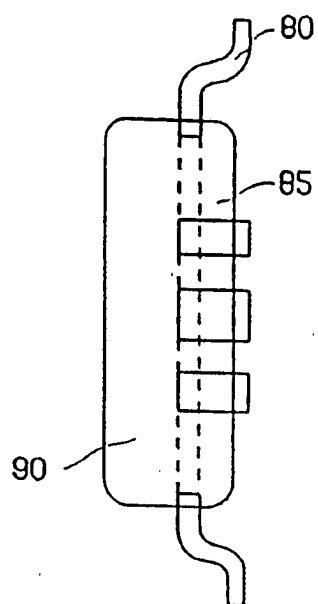


FIG. 4f

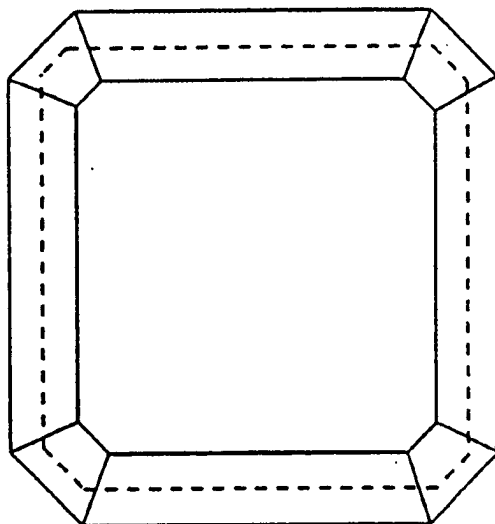


FIG. 4g

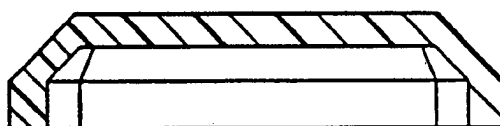


FIG. 4h

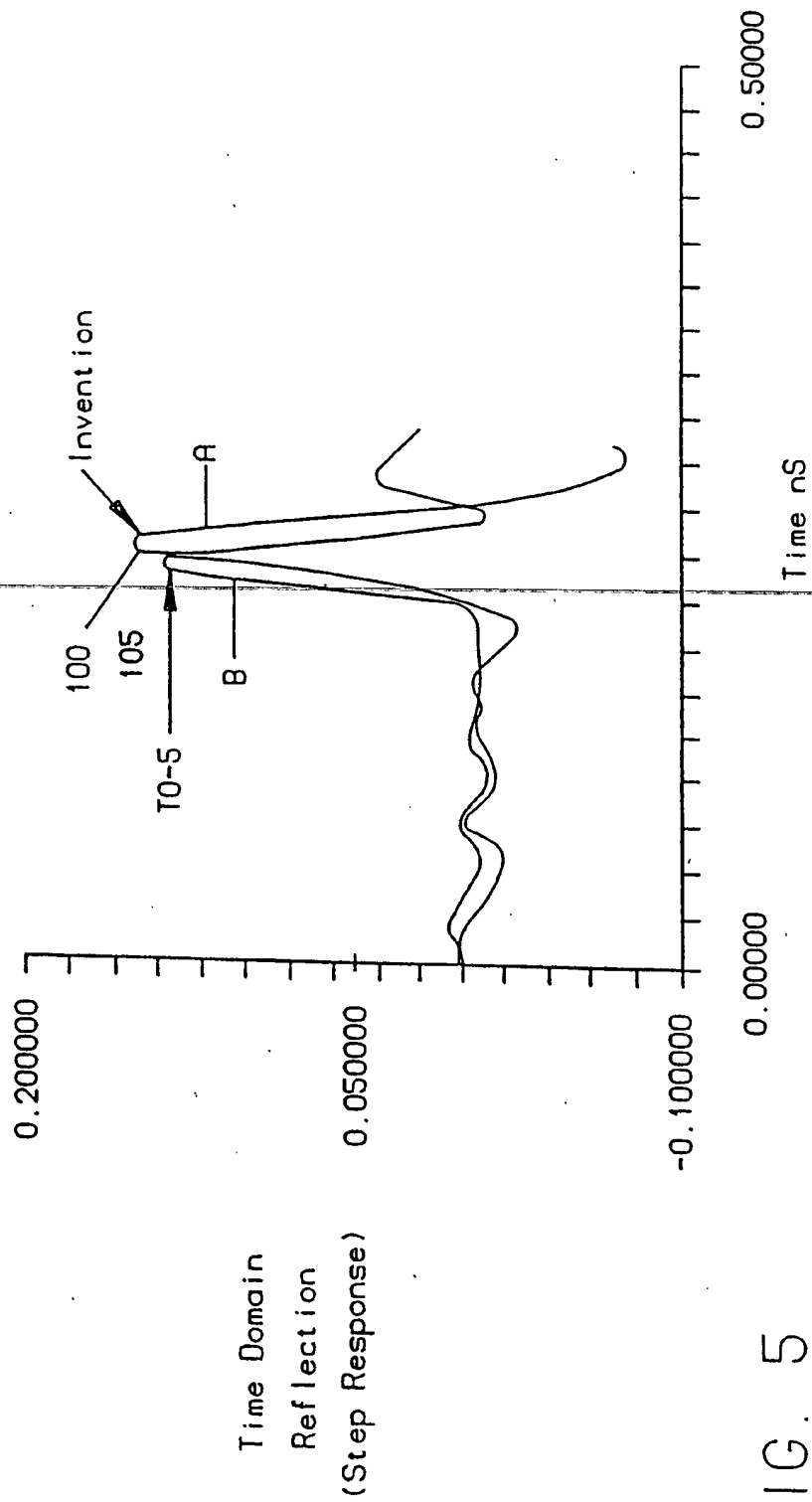


FIG. 5

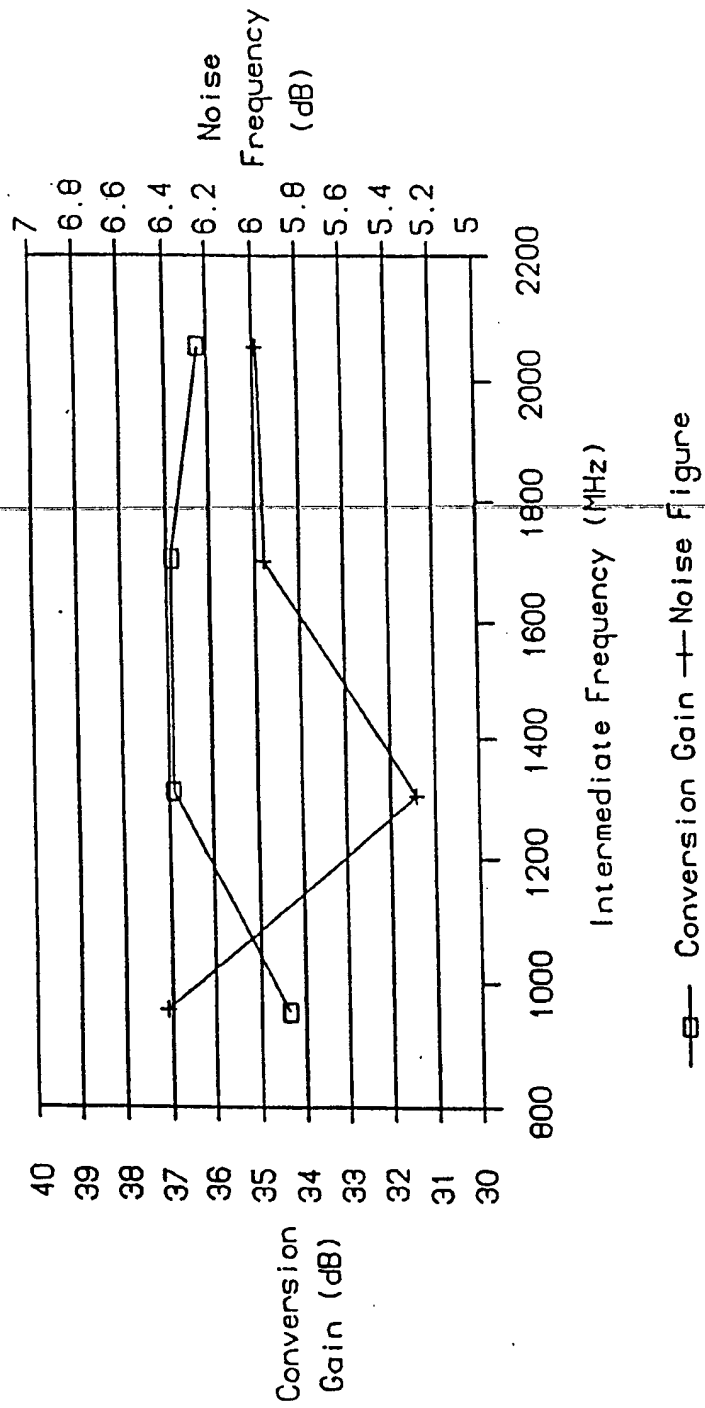


FIG. 6



## INTERNATIONAL SEARCH REPORT

International application No.

PCT/US94/01056

## A. CLASSIFICATION OF SUBJECT MATTER

IPC(5) : HO1L 23/28, 23/48, 29/40

US CL : 257/666, 664, 691

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 257/666, 664, 691

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

APS

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US, A, 4,680,613, (DANIELS ET AL.) 14 July 1987, col. 6, line 55 to col. 7, line 37.	1
Y		2-33
Y	US, A, 3,489,956 (YANAI ET AL.) 13 January 1970, figure 3, col. 5, line 54 to col. 6, line 9, col. 7, lines 33-52.	1-33
Y	US, A, 3,628,105 (SAKAI ET AL.) 14 December 1971, figure 9, col. 4, lines 27-42.	15-33
A	US, A, 5,162,896 (TAKUBO ET AL.) 10 November 1992, figures 3 and 8.	1-33
A	US, A, 4,441,119 (LINK) 3 April 1984, figure 1.	1-33

☐ Further documents are listed in the continuation of Box C.☐ See patent family annex.

* Special categories of cited documents:	* T	later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
* A		document defining the general state of the art which is not considered to be part of particular relevance
* E		earlier document published on or after the international filing date
* L		document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
* O		document referring to an oral disclosure, use, exhibition or other means
* P		document published prior to the international filing date but later than the priority date claimed
	* X	document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
	* Y	document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art
	* A*	document member of the same patent family

Date of the actual completion of the international search

25 March 1994

Date of mailing of the international search report

APR 04 1994

Name and mailing address of the ISA/US  
Commissioner of Patents and Trademarks  
Box PCT  
Washington, D.C. 20231

Facsimile No. NOT APPLICABLE

Authorized officer

TERESA ARROYO *Smully*  
Telephone No. (703) 308-0956